

## **EDAptability: System-Prototyping-Emulator hardware with unique structure targets new standard**

*World fastest system prototyping emulator hardware with amazing benefits*

MUNICH, Germany, August, 3rd, 2007 - EDAptability today announced the availability of its new system-prototyping-emulator (SPE) hardware. The unique structure results from the compiled experiences at various projects, applications, customers and shortcomings of comparable system prototyping hardware.

EDAptability gives the RACERS-guarantee on the hardware. R: The best possible switch and/or fixed routing concept, A: access to all FPGA IOs, Vrefs, ..., C: lowest system prototyping costs per FPGA in the industry, E: FPGA-wise extendable, R: successively replaceable by new FPGAs and S: speed, the fastest System-Prototyping-Emulator hardware in the industry.

It is the only one of its kind to support 1..10 Altera and/or Xilinx devices by keeping full system compatibility. For each project, the user can select the mixture of switch or fixed routing. All FPGA pins are directly accessible (no switch) and all voltages can be driven alternatively from outside. The unique clock structure leaves it to the project to drive any of the 32 clocks per FPGA individually or by one of the 32 global clocks. The SPE is capable of providing 333 MHz DDR based connectivity btw. each FPGA (e.g. 1200 IOs) due to its patent pending structure. All IOs can be connected to an external logic analyzer. The SPE has also an integrated logic analyzer for sampling 96 signals, trigger functionality and on-board 2GB for VCD file dump. The number of sampling pins become almost unlimited if pin multiplexing is used.

"I'm very, very proud of this product. After working as an FAE the last years I got more and more frustrated of products, which didn't really go for state-of-the-art customer solutions. Now customers can go for long awaited prototyping solutions which are possible nowadays." said Tobias Strauch, developer at EDAptability.

EDAptability provides application specific boards, which can be connected to the SPE and which serve special functionality (USB, PCIe, ...). These application boards are made on customer request and are free of charge (except material costs) if can be used on other projects. EDAptability also offers service to tightly couple customer logic on the FPGA boards for high-end connectivity (DDR2, RocketIO, ...).

### **About EDAptability**

EDAptability provides leading edge EDA tools for the complete ASIC and FPGA market. EDAptability's key technologies reach from unique simulator techniques to RTL-to-RTL partitioner techniques as well as outstanding system prototyping hardware. Each component can be seen as ahead of the competitors and gives direct benefit to the customer. For more information visit [www.EDAptability.com](http://www.EDAptability.com).

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